

P910351

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Hsu et al.

U.S. Serial No: 10/627,000

Filed: July 25, 2003

For: METHOD FOR FORMING NON-VOLATILE MEMORY CELL WITH LOW-TEMPERATURE-FORMED DIELECTRIC BETWEEN WORD AND BIT LINES, AND NON-VOLATILE MEMORY ARRAY INCLUDING SUCH MEMORY CELLS

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/ Examiner: Schillinger, Laura M.
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/ Group Art: 2813

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I hereby certify that this paper is being facsimile transmitted to: Commissioner for Patents at fax number 571-273-8300 on October 19, 2005.

Kenton R. Mullins, Reg. No. 36,331

AMENDMENT

Dear Sir:

In further response to the Office Action mailed July 20, 2005 and the Advisory Action, Applicants respectfully request that the following amendment be entered in the above-identified parent application:

Listing of the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.